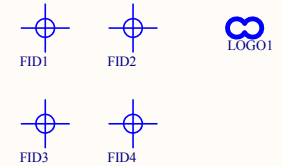


Design Notes

- 1) Connector header (schematic symbol) signal direction is with respect to a single module (not the carrier direction).
- 2) For simplicity, and to keep carrier at 4 layers with contiguous GROUND planes (2 GND layers), AC coupled termination is implemented instead of Thevinin. The actual termination devices are not installed. Recommend use of series termination at driver (if termination is required at all). Candidate signals for termination are CLK0, CLK1, SCLK1, SCLK2, SCLK3, SCLK4, WR, SPICLK0, SPICLK1.
- 3) Leave ends of carrier free of components and bare copper. This will allow insertion into an enclosure (PCB ends slide into card guide or extrusion).

TABLE OF CONTENTS:

- 1- Cover Sheet
- 2- Header Connectors Row A
- 3- Header Connectors Row B
- 4- Header Connectors Row G
- 5- Header Connectors Row H



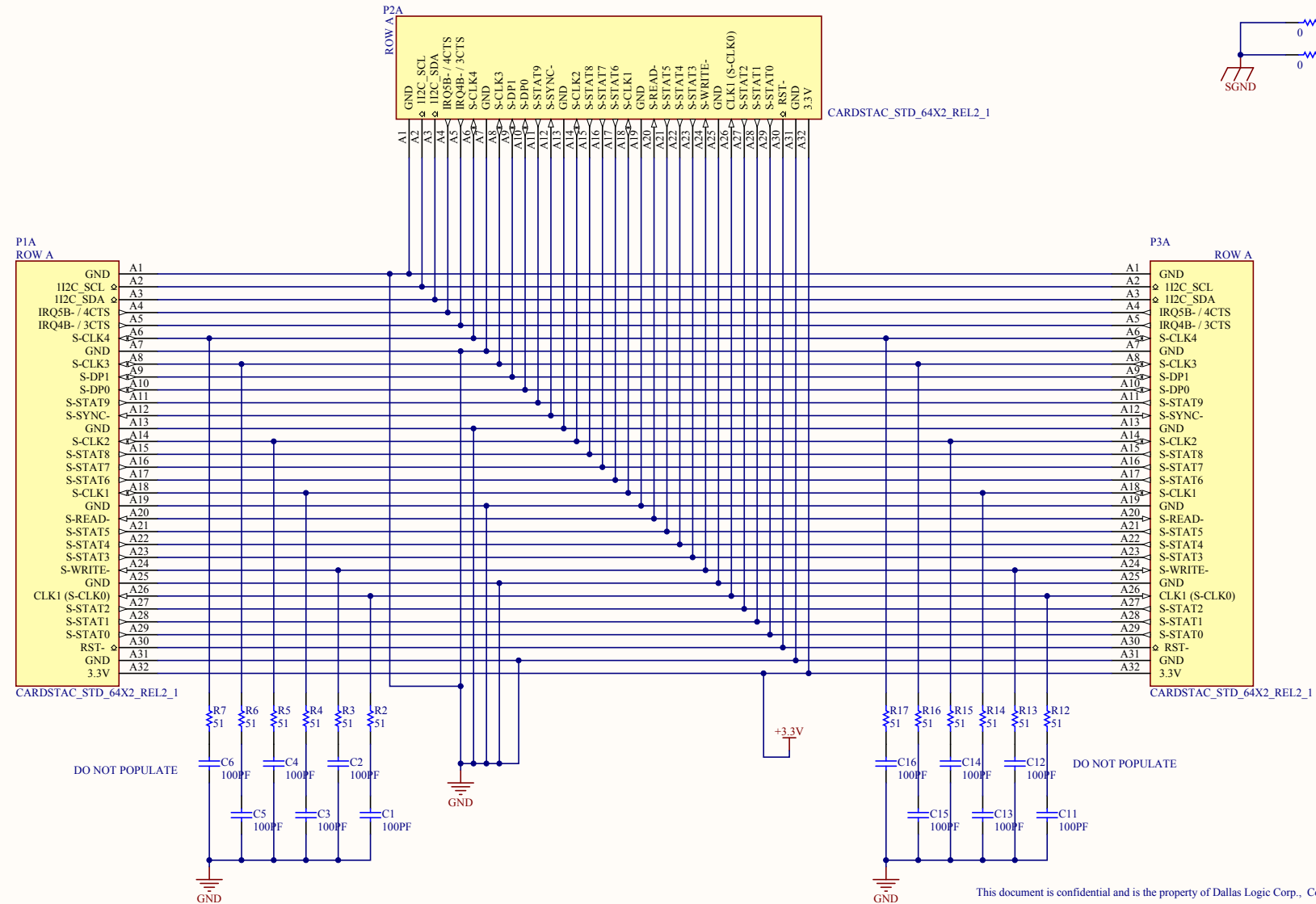
REVISION TABLE			
08-06-10	A	ET	Initial release of the schematic.

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Title: Carrier, Horizontal, Three Position			Dallas Logic Corporation 2300 McDermott Rd. #200-305 Plano TX 75025 USA		
Size: B	Number: CC23001	Revision: A			
Date: 8/6/2010	Time: 1:35:52 AM	Sheet 1 of 5	File: C:\dallas_logic\altium_dlc\Projects\CC23001_REVA\1_TOC.SchDoc		

Connector header (schematic symbol) signal direction is with respect to a single module (not the carrier direction).

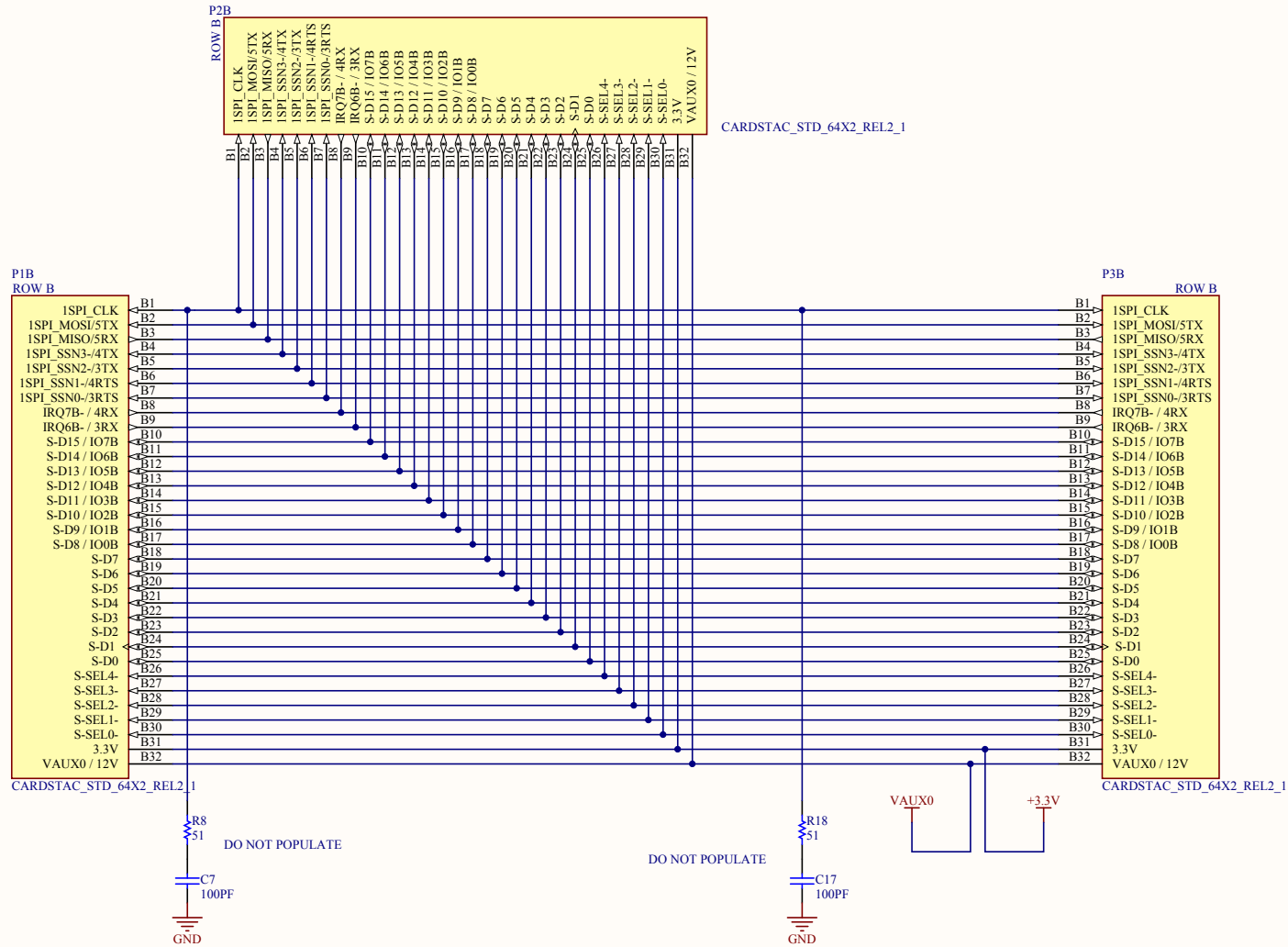
SGND (Shield GND or guard ring) connected to ground power pins at four corners of carrier.



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Title Carrier, Horizontal, Three Position			Dallas Logic Corporation 2300 McDermott Rd. #200-305 Plano TX 75025 USA	
Size: B	Number: CC23001	Revision: A		
Date: 8/6/2010	Time: 1:35:52 AM	Sheet 2 of 5		
File: C:\dallas logic\altium dlc\Projects\CC23001 REVA2_HDR_A.SchDoc				

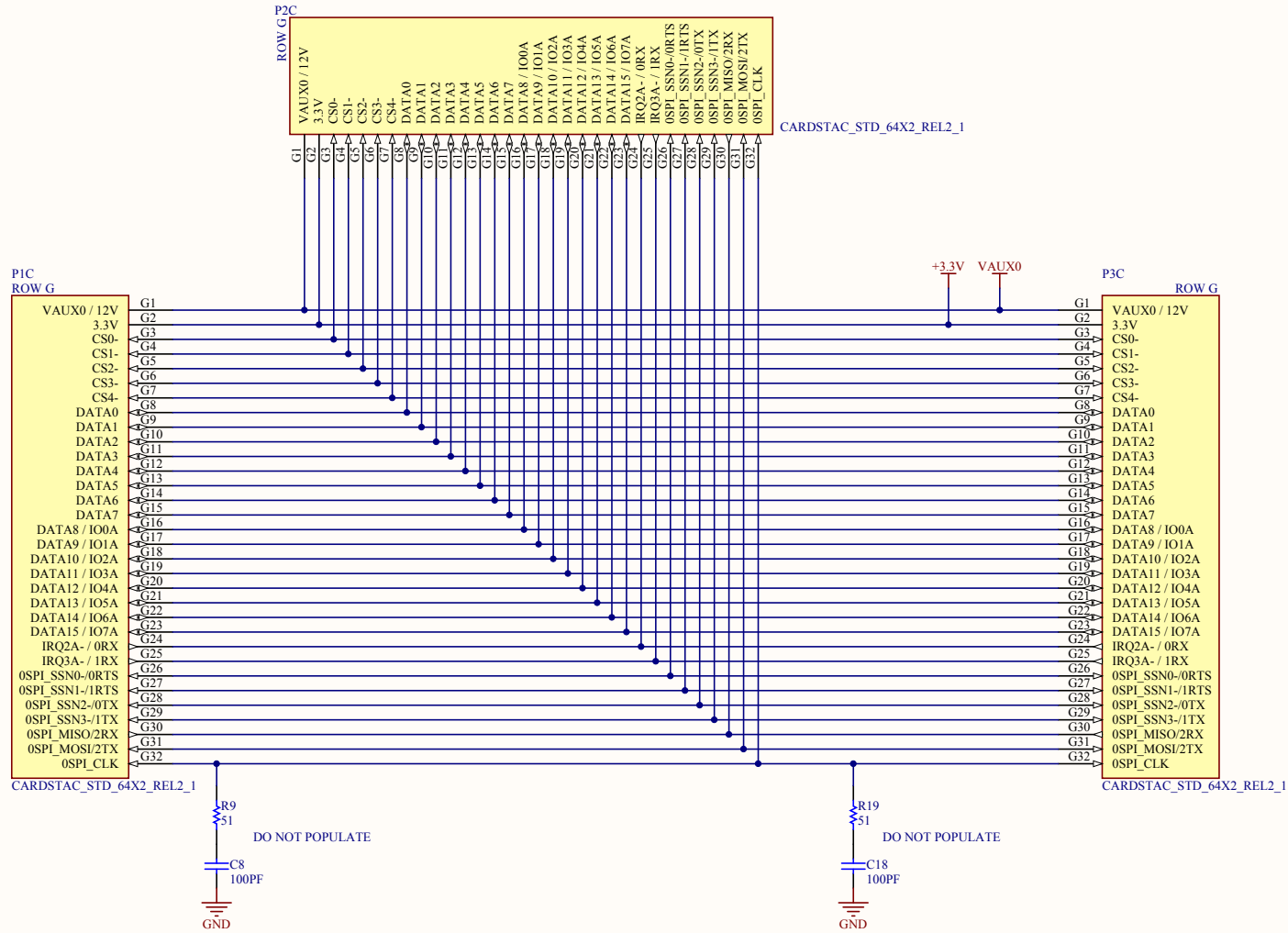
Connector header (schematic symbol) signal direction is with respect to a single module (not the carrier direction).




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Title: Carrier, Horizontal, Three Position			Dallas Logic Corporation 2300 McDermott Rd. #200-305 Plano TX 75025 USA	
Size: B	Number: CC23001	Revision: A		
Date: 8/6/2010	Time: 1:35:52 AM	Sheet 3 of 5	File: C:\dallas_logic\altium_dlc\Projects\CC23001_REVA\3_HDR_B.SchDoc	

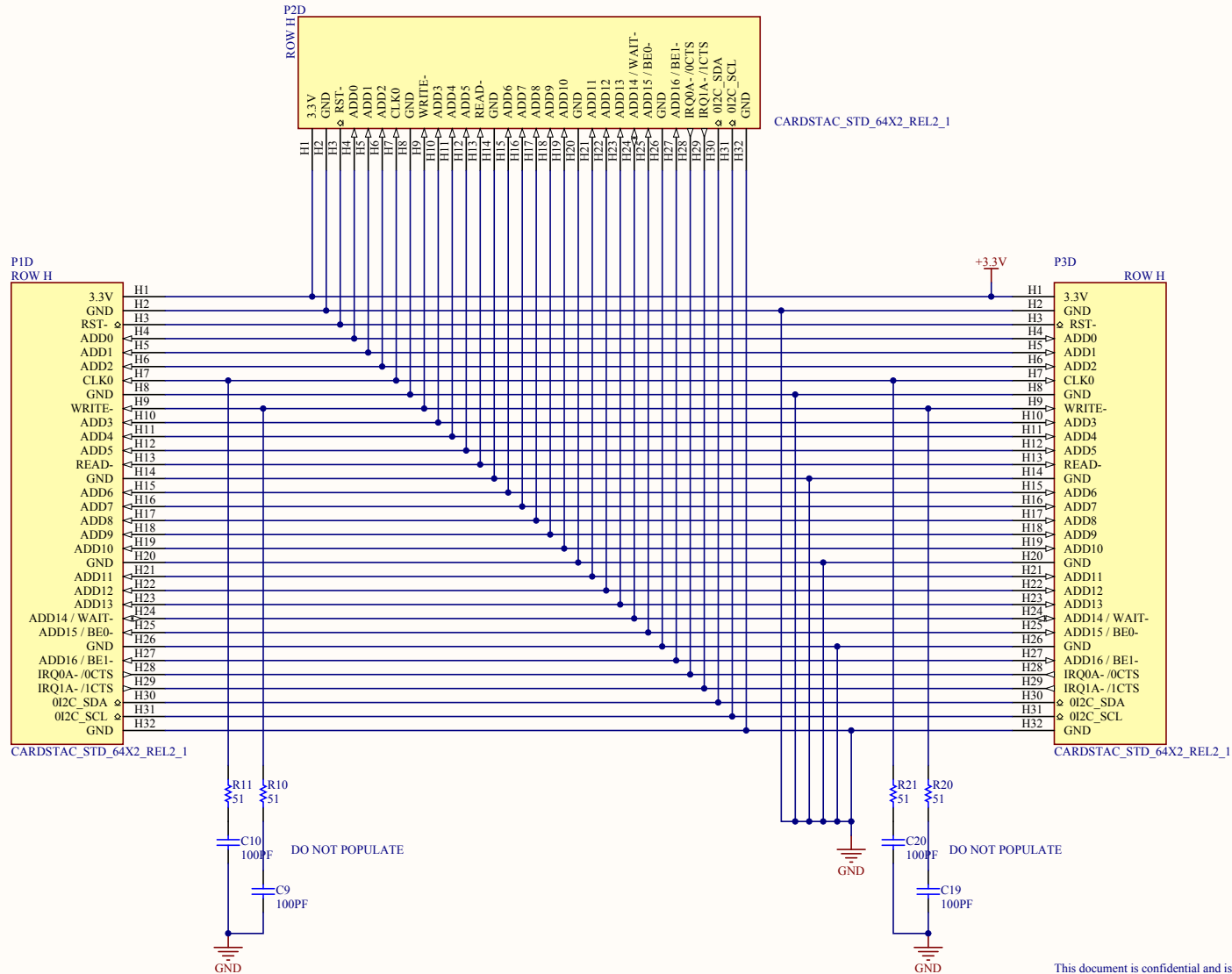
Connector header (schematic symbol) signal direction is with respect to a single module (not the carrier direction).



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Title Carrier, Horizontal, Three Position			Dallas Logic Corporation 2300 McDermott Rd. #200-305 Plano TX 75025 USA	
Size: B	Number: CC23001	Revision: A		
Date: 8/6/2010	Time: 1:35:52 AM	Sheet 4 of 5	File: C:\dallas_logic\altium_dlc\Projects\CC23001_REVA\4_HDR_G.SchDoc	

Connector header (schematic symbol) signal direction is with respect to a single module (not the carrier direction).



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Size: B	Number: CC23001	Revision: A		
Date: 8/6/2010	Time: 1:35:52 AM	Sheet 5 of 5		
File: C:\dallas logic\altium_dlc\Projects\CC23001_REVA\5_HDR_H.SchDoc				